

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Samir Chaudhry, et al.

Serial No.: N/A

Filed: Herewith

Title: MOS TRANSISTOR AND METHOD OF MANUFACTURE

Grp./A.U.: N/A

Examiner: N/A

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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1-22-2004	<i>Elizabeth Schumacher</i>
Date of Mailing	Signature of person mailing

Sir:

PRELIMINARY AMENDMENT

Before examination of the above-identified application, please amend it as follows: